April 7 , 2004

Commissioner for Patents P.O.Box 1450 Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572 28 Davis Avenue Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/706,837 11/12/03

Dr. Axel Pannwitz

FREQUENCY COMPENSATION SCHEME FOR LOW DROP OUT VOLTAGE REGULATORS USING ADAPTIVE BIAS

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation In An Application.

The following Patents and/or Publications are submitted to comply with the duty of disclosure under CFR 1.97-1.99 and 37 CFR 1.56.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on April 12, 2004.

Stephen B. Ackerman, Reg.# 37761

Signature/Date Spl. 9/12/04

DS-03-015

U.S. Patent 6,246,221 to Xi, "PMOS Low Drop-Out Voltage Regulator Using Non-Inverting Variable Gain Stage," discusses a high power supply ripple rejection (PSRR) internally compensated low drop-out voltage regulator using an output PMOS pass device.

European Patent Application EP 0 779 568 A2 to Danstrom,
"Programmable bandwidth voltage regulator," discloses a method
for reducing the transient response time of a voltage regulator
when the load attached to it is entering or exiting a lower
power consumption level by changing the bandwidth of the
voltage regulator without compromising its stability, and a
bandwidth regulator for implementing such a method.

U.S. Patent 6,603,292 to Schouten et al., "LDO Regulator Having an Adaptive Zero Frequency Circuit," describes a low dropout voltage (LDO) regulator having an adaptive zero frequency circuit.

European Patent Application EP 0 862 102 A1 to Edwards, "Load pole stabilized voltage regulator," discloses a voltage regulator with load pole stabilization.

Sincerely,

Stephen B. Ackerman,

Reg. No. 37761

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